Docket No. 740819-634

# IN T

# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of:

Koji ARITA et al. ) Art Unit: 2813

Serial No. 09/938,528

Filed: 08/27/2001 ) Examiner: D. Blum

For: METHOD FOR MANUFACTURING A)

SEMICONDUCTOR DEVICE

### CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner of Patents, Washington, D.C. 20231 on

1.30.2002 Adu

Adele M. Stamper

## **AMENDMENT**

**Assistant Commissioner for Patents** 

Washington, D.C. 20231

Sir:

In response to the Office Action mailed April 1, 2002, please amend the above identified application as follows.

# **IN THE CLAIMS:**

Please cancel claims 2 and 3.

Please amend claim 1 as follows:

SUB, 1. (Amended) A

A method for manufacturing a semiconductor device,

comprising the steps of:

forming, in a semiconductor layer formed on a first insulating film, an element isolation groove extending to the first insulating film;

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